

PC930 Series

Digital Output, High Sensitivity Type OPIC Photocoupler

Features

- High sensitivity
($I_{FLH}, I_{FHL} : \text{MAX. 1mA}$)
- TTL and LSTTL compatible output
- Operating supply voltage range
($V_{CC} : 4.5 \text{ to } 15\text{V}$, **PC930/PC931/PC932/PC933**)
- Various output forms
(Open collector output, pull-up resistor built-in type, totem pole output)
- Low output current dissipation
($I_{CCL} : \text{MAX. 3.8mA}$)
- High isolation voltage between input and output ($V_{i-o} : 5000\text{V}_{rms}$)
- Recognized by UL, file No. E64380

Model Line-up

	Open collector output type	Pull-up resistor built-in type	Totem pole output type
Low active	PC930	PC932	PC934
High active	PC931	PC933	PC935

Applications

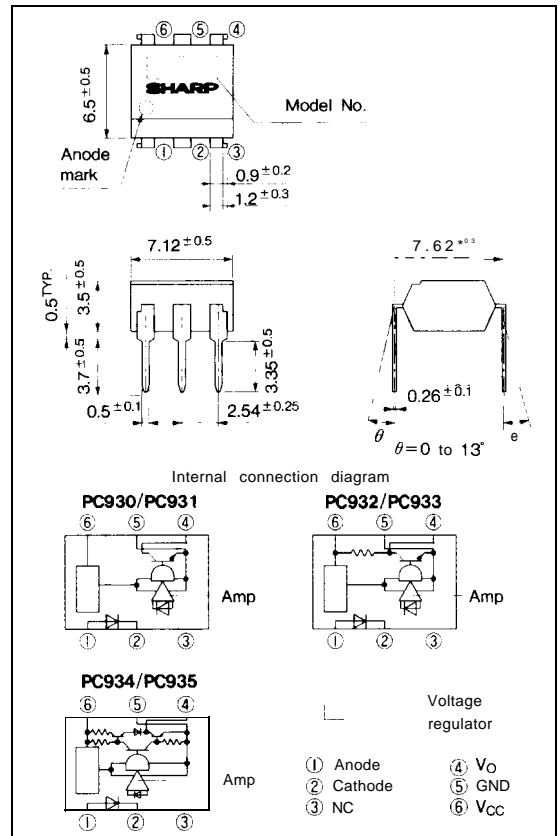
- Computer terminals
- High speed line receivers
- Interfaces with various data transmission equipment

Absolute Maximum Ratings

Parameter		Symbol	Rating	Unit	
Input	Forward current	I_F	20	mA	
	*1 Peak forward current	I_{FM}	1	A	
	Reverse voltage	V_R	6	v	
	Power dissipation	P	70	mW	
output	Supply voltage	V_{CC}	PC930/PC931 PC932/PC933	-0.5 to 16.0	V
			PC934/PC935	-0.5 to 7.0	
	High level output voltage	PC930/PC931	V_{OH}	-0.5 to 16.0	V
	High level output current	PC934/PC935	I_{OH}	-800	μA
	Low level output current	$ I_{OL} $	50	mA	
	Power dissipation	P_O	150	mW	
Total power dissipation		P_{tot}	170	mW	
*Isolation voltage		V_{iso}	5000	V_{rms}	
Operating temperature		T_{opr}	-25 to -85	$^{\circ}\text{C}$	
Storage temperature		T_{str}	-40 to +125	$^{\circ}\text{C}$	
*Soldering temperature		T_{sol}	260	$^{\circ}\text{C}$	

outline Dimensions

(Unit : mm)



*"OPIC" (optical IC) is a trademark of the SHARP Corporation. An OPIC consists of a light-detecting element and signal processing circuit integrated onto a single chip.

*1 Pulse width $\leq 100 \mu\text{s}$

Duty ratio ~ 0.001

*2 40 to 60%RH, AC for 1 minute

*3 For 10 seconds

■ Electro-optical Characteristics

(Ta = 0 to + 70°C unless otherwise specified.)

			Conditions	MIN.	TYP.	MAX.	Unit			
Input	Forward voltage		V_F	$I_F = 2\text{mA}$	1.1	1.4	V			
	Reverse current		I_R	$I_F = 0.1\text{mA}$	0.55	0.95	V			
	Terminal capacitance		C_T	$T_a = 25^\circ\text{C}, V_R = 3\text{V}$	—	—	10	μA		
Output	Operating supply voltage	PC930/PC931 PC932/PC933	V_{CC}	$T_a = 25^\circ\text{C}, V = 0, f = 1\text{kHz}$	—	30	250	pF		
		PC934/PC935			4.5	—	15	V		
	Low level output voltage	PC930/PC932	V_{OL}	$I_{OL} = 16\text{mA}, V_{CC} = 5\text{V}, I_F = 1\text{mA}$	—	0.15	0.4	v		
		PC931/PC933							$I_{OL} = 16\text{mA}, V_{CC} = 5\text{V}, I_F = 0$	
		PC934								$I_{OL} = 16\text{mA}, V_{CC} = 4.5\text{V}, I_F = 1\text{mA}$
		PC935								
	High level output voltage	PC932	V_{OH}	$V_{CC} = 5\text{V}, I_F = 0$	3.5	—	V			
		PC933						$V_{CC} = 5\text{V}, I_F = 1\text{mA}, I_{OH} = -400\mu\text{A}$		
		PC934							$V_{CC} = 4.5\text{V}, I_F = 0, I_{OH} = -400\mu\text{A}$	
		PC935								$V_{CC} = 4.5\text{V}, I_F = 1\text{mA}, I_{OH} = -400\mu\text{A}$
	High level output current	PC930	I_{OH}	$V_{CC} = V_O = 15\text{V}, I_F = 0$	—	100	μA			
		PC931						$V_{CC} = V_O = 15\text{V}, I_F = 1\text{mA}$		
Low level supply current	PC930	I_{CC1}	$V_{CC} = 5\text{V}, I_F = 1\text{mA}$	—	1.3	3.4	mA			
	PC931							$V_{CC} = 5\text{V}, I_F = 0$		
	PC932/PC934								$V_{CC} = 5\text{V}, I_F = 1\text{mA}$	
	PC933/PC935									$V_{CC} = 5\text{V}, I_F = 0$
High level supply current	PC930/PC932 PC934	I_{CC2}	$V_{CC} = 5\text{V}, I_F = 0$	—	0.7	2.2	mA			
	PC931/PC933 PC935							$V_{CC} = 5\text{V}, I_F = 1\text{mA}$		
	PC934								$V_{CC} = 5\text{V}, I_F = 0, T = \text{Within 1 second}$	
PC935	$V_{CC} = 5\text{V}, I_F = 1\text{mA}, T = \text{Within 1 second}$									
Output short circuit current		PC934	I_{OS}	6	17	35	mA			
		PC935								
Transfer characteristics	*4 "High → Low" Threshold input current	PC930/PC932 PC934	I_{FHL}	$V_{CC} = 5\text{V}, R_L = 280\Omega$	0.5	1.0	mA			
		PC931/PC933 PC935			0.1	0.4				
	*5 "Low → High" Threshold input current	PC930/PC932 PC934	I_{FLH}	$V_{CC} = 5\text{V}, R_L = 280\Omega$	0.1	0.4	mA			
		PC931/PC933 PC935			—	0.5		1.0		
	*6 Hysteresis	PC930/PC932 PC934	I_{FLH}/I_{FH}	$V_{CC} = 5\text{V}, R_L = 280\Omega$	—	0.8	—			
		PC931/PC933 PC935	I_{FHL}/I_{FL}							
	Isolation resistance			R_{ISO}	$T_a = 25^\circ\text{C}, DC 500\text{V}, 40 \text{ to } 60\%RH$	5×10^{10}	10^{11}	Ω		
	Response time	"High → Low" propagation delay time	PC930/PC932 PC934	t_{PHL}	$T_a = 25^\circ\text{C}$ $V_{CC} = 5\text{V}$ $I_F = 1\text{mA}$ $R_L = 280\Omega$ Fig.1	—	3	9	μs	
			PC931/PC933 PC935			—	5	15		
		"Low → High" propagation delay time	PC930/PC932 PC934	t_{PLH}		5	15			
			PC931/PC933 PC935			3	9			
		Fall time				t_f	—	0.05		0.5
				—	0.1	0.5				

*4 I_{FHL} represents forward current when output goes from high to low

*5 I_{FLH} represents forward current when output goes from low to high.

*6 Hysteresis stands for I_{FLH}/I_{FH} .

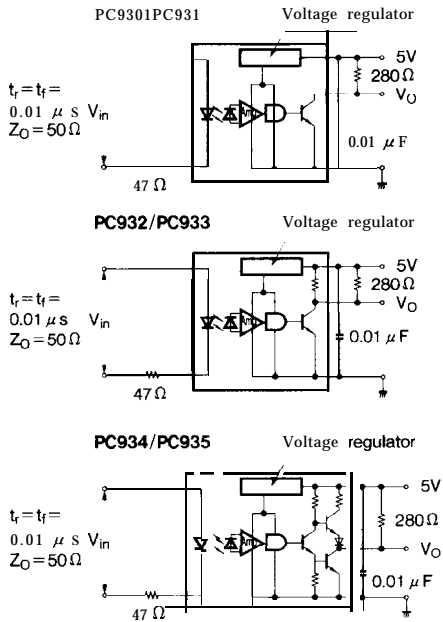
6

Photocouplers

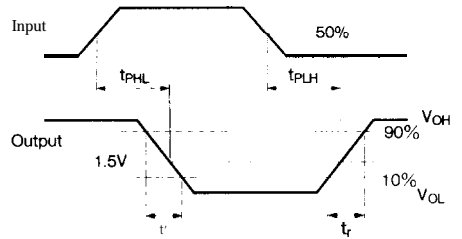
Recommended Operating Conditions

Parameter	Symbol	MIN.	TYP.	MAX.	Unit	
Low level output current	I_{OL}		1.6	16	mA	
High level output current	PC934/PC935	I_{OH}	-	-400	μA	
supply voltage	PC930/PC931	V_{CC}	4.5	5.0	15.0	v
	PC932/PC933					V
	PC934/PC935					V
Operating temperature	T_{opr}	0	25	70	$^{\circ}C$	

Fig. 1 Test Circuit for t_{PHL} , t_{PLH} , t_r , t_f



PC930/PC932/PC934



PC931/PC933/PC935

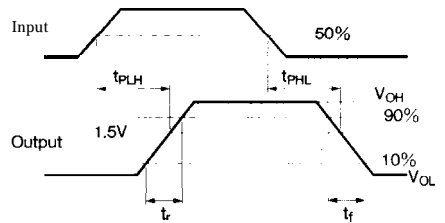


Fig. 2 Forward Current vs. Ambient Temperature

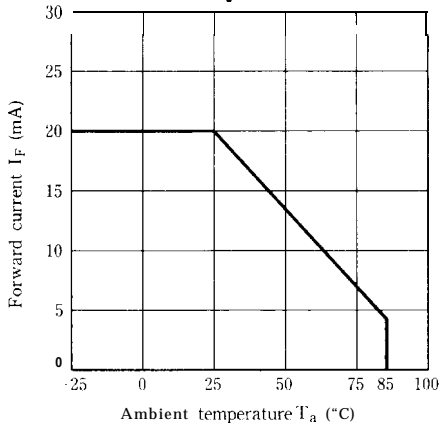


Fig. 3 Power Dissipation vs. Ambient Temperature

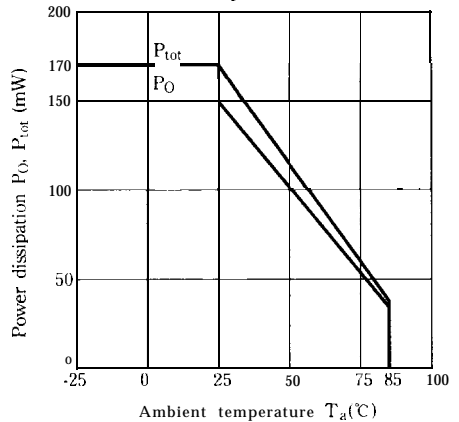


Fig. 9-a Supply Current vs. Supply Voltage (PC930/PC931)

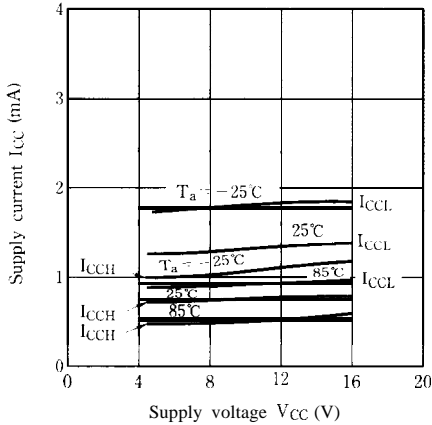


Fig. 9-b Supply Current vs. Supply Voltage (PC932/PC933)

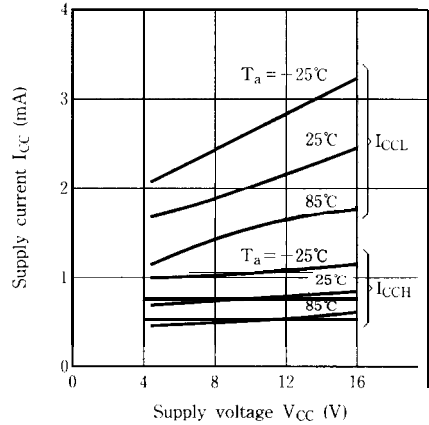


Fig. 9-c Supply Current vs. Supply Voltage (PC934/PC935)

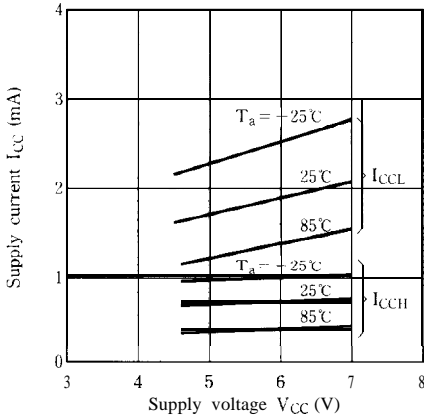


Fig.10 Propagation Delay Time vs. Forward Current

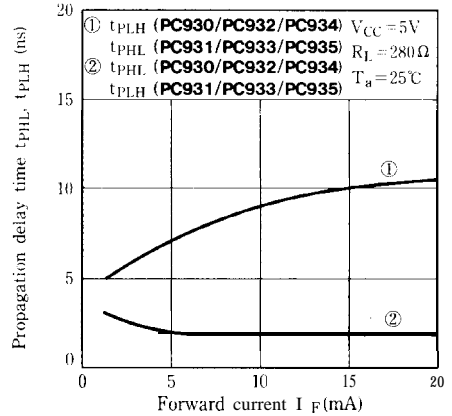


Fig.11-a Rise Time, Fall Time vs. Load Resistance

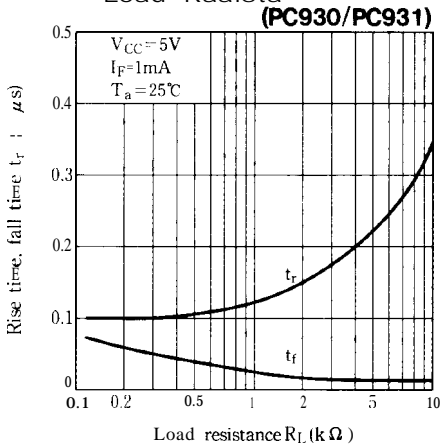


Fig.11-b Rise Time, Fall Time vs. Load Resistance

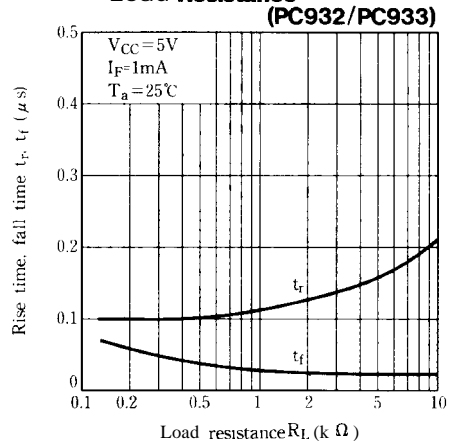
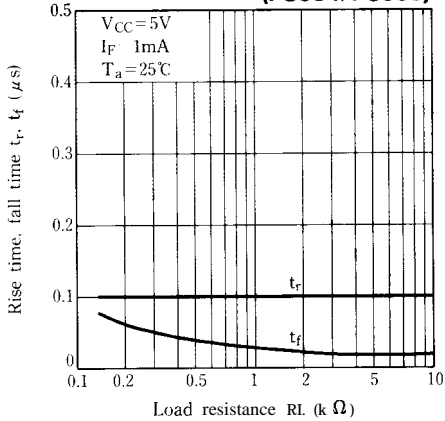


Fig.11-c Rise Time, Fall Time vs. Load Resistance

(PC934/PC935)



■ Precautions for Use

- (1) It is recommended that a by-pass capacitor of more than $0.01 \mu F$ is added between V_{CC} and GND near the device in order to stabilize power supply line.
- (2) Handle this product the same as with other integrated circuits against static electricity.
- (3) As for other general cautions, refer to the chapter "Precautions for Use." (Page 78 to 93)

